

ABSTRACT OF THE DISCLOSURE

A method and a device for correcting the phase between the pixel clock of a graphics card and the sampling clock of a flat-panel display with an analog interface in a system including a flat-panel display, graphics card, and computer. Automatic adjustment of the phase is performed repeatedly. In the process, the rising edge of a video pulse of a sufficiently bright image spot in the first image column close to the back-porch region is determined. The falling edge of a video pulse is determined at a sufficiently bright image spot in the last image column close to the front-porch region, and the phase is adjusted such that the sampling instant is situated approximately at the midpoint between the rising and falling edges of a video pulse.

REMARKS

Favorable consideration of this application, as presently amended, is respectfully requested.

The present preliminary amendment is submitted to place the above-identified application in more proper format under United States practice.

By the present preliminary amendment the title has been amended to match that on the PCT publication.

The claims have been amended by the present response to no longer recite any improper multiple dependencies or reference numerals.

The Abstract has been amended by the present response to be in more proper format under United States practice.